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Application Number	10/652,135
Filing Date	8/29/2003
First Named Inventor	May
Art Unit	2183
Examiner Name	Meonske, Tonja
Attorney Docket Number	CML00769D

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
TM/		3,718,912	Feb-73	Hasbrouck et al.	
		4,128,880	Dec-78	Cray Jr.	
		4,744,043	May-88	Kloker	
		4,760,545	Jul-88	Inagami et al.	
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		4,918,600	Apr-90	Harper et al.	
		5,206,822	Apr-93	Taylor	
		5,367,494	Nov-94	Shebanow et al.	
		5,390,352	Feb-95	Kinoshita	
		5,450,607	Sep-95	Kowalczyk et al.	
		5,495,617	Feb-96	Yamada	
		5,652,909	Jul-97	Kodosky	
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		5,881,257	Mar-99	Glass et al.	
		5,881,263	Mar-99	York et al.	
		5,887,183	Mar-99	Agarwal et al.	
		5,893,143	Apr-99	Tanaka et al.	
		5,936,953	Aug-99	Simmons	

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/Tonia Meonske/

Date
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Substitute for form 1449A/PTO				Complete if Known Application Number 10/652,135 Filing Date 8/29/2003 First Named Inventor May Art Unit 2183 Examiner Name Meonske, Tonia Attorney Docket Number CML00769D	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>					
Sheet	2	of	2		

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³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.
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Substitute for form 1449A/PTO				Complete if Known Application Number 10/652,135 Filing Date 8/29/2003 First Named Inventor May Art Unit 2183 Examiner Name Meonske, Tonia Attorney Docket Number CML00769D	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>					
Sheet	1	of	1		

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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ²
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	
				Filing Date	
				First Named Inventor Philip E. MAY	
				Art Unit	
				Examiner Name	
Sheet	1	of	1	Attorney Docket Number CML00769D	

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
/TMM/		US-20020112228 A1	08-15-2002	Granston et al.	
		US-20020120923 A1	08-29-2002	Granston et al.	
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Examiner Signature	/Tonia Meonske/	Date Considered	05/31/2007
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Substitute for form 1449B/PTO		Complete if Known	
		Application Number	10/652,135
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Filing Date	8/29/2003
		First Named Inventor	May
		Art Unit	2183
		Examiner Name	Meonske, Tonia
		Attorney Docket Number	CML00769D
Sheet	1	of	1

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cita No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/TM/		LAM, M., Software Pipelining: An Effective Scheduling Technique for VLIW Machines, Proceedings of the SIGPLAN '88 Conference on Programming Language Design and Implementation, Atlanta, Georgia, June 22-24, 1988, pp. 318-328	
		LEE, T. et al., A Transformation-Based Method for Loop Folding, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 13, No. 4, April 1994, pp. 439-450	
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		COOPER, K.D. et al., Efficient Computation of Flow Insensitive Interprocedural Summary Information, SIGPLAN Notices, Vol. 19, No. 6, June 1984, pp. 247-258	
		STRONG, H.R., Vector Execution of Flow Graphs, Journal of the Association for Computing Machinery, Vol 39, No. 1, January 1983, pp. 186-196	
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		TALLA, D., Bottlenecks in Multimedia Processing with SIMD Style Extensions and Architectural Enhancements, IEEE Transactions of Computers, August 2003, pp. 1015-1031, Vol. 62, No. 8, IEEE	

Examiner Signature	/Tonia Meonske/	Date Considered	05/31/2007
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Substitute for form 1449B/PTO		Complete If Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	10/652,135
		Filing Date	8/29/2003
		First Named Inventor	May
		Art Unit	2183
		Examiner Name	Meonske, Tonia
		Attorney Docket Number	CML00769D
Sheet	1	of	1

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cita No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/TM/		Al-Mouhamed, M., "Lower Bound on the Number of Processors and Time for Scheduling Precedence Graphs with Communication Costs", IEEE, December 1990	
/TM/		Samadzadeh, F. et al., "A Heuristic Multiprocessor Scheduling Algorithm for Creating Near-Optimal Schedules Using Task System Graphs", ACM, 1992	
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/TM/		Kwok, Y. et al., "Static Scheduling Algorithms for Allocating Directed Task Graphs to Multiprocessors", ACM, December 1999	

Examiner Signature	/Tonia Meonske/	Date Considered	05/31/2007
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